In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge the underpayment to Deposit Account No. 50-2215.

CONTINGENT EXTENSION REQUEST

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 C.F.R. § 1.136(a), to extend the time for filing a response to the outstanding Office Action by the number of months which will avoid abandonment under 37 C.F.R. § 1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 50-2215.

AMENDMENTS

In the Claims:

Please cancel claim 13, without prejudice.

Please amend claims 1, 2, 7 and 14 and add new claim 15 pursuant to 37 C.F.R. § 1.121(c)(1)(i) as set forth in the "clean" version set forth below. Entry is respectfully requested. A version with markings to show the changes made pursuant to 37 C.F.R. § 1.121(c)(1)(ii) is attached hereto as Appendix A.

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1. (Twice Amended) A video data transfer system comprising:

a real time output path through which video data processed by a video processor is sent to a display via a frame buffer;

a capturing path which is independent of said real time output path and through which said video data is sent to a system memory via a system bus and not through the frame buffer; and

a gate in said capturing path, said gate being controllable to permit said video data to pass when received from said video processor.

2. (Twice Amended) A video data transfer system, comprising:

a real time output path through which video data processed by a video processor is sent to a display via a frame buffer;

a capturing path which is independent of said real time output path and through which said video data is sent to a system memory via a system bus and not through the frame buffer, wherein

said real time output path comprises:

an off-screen memory which receives video data from said video processor via a data bus and stores video data therein, said off-screen memory being in the frame buffer; and

a display control circuit which receives video data read from said off-screen memory via said data bus for enlargement and interpolation processing and transfers processed results to said display, and wherein

said capturing path comprises:

a gate which is opened only when video data is received from said video processor for capturing; and

memory means for storing said video data sent through said gate and for transferring said video data to said system bus.

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7. (Twice Amended) A video data transfer method, comprising:

provideproviding video data from a video processor to a plurality of paths independent of each other;

sending said video data to a display through a frame buffer in at least one of said independent paths operating as a real time output path;

sending said video data to a system memory through a system bus and not through the frame buffer in at least another of said independent paths operating as a capture path; and

controlling said capture path to permit said video data to pass to said system memory when said video data is to be captured.

14. (Amended) A method of transferring video data, the method comprising:

receiving video data;

determining whether the video data is to be captured;

processing the video data to produce processed data;

forwarding the processed video data to a first path, the first path including a display control circuit and a frame buffer; and

forwarding the processed video data to a second path when the determining indicates that the video data is to be captured, the second path not including the frame buffer;

wherein the first and second paths are distinct.

15. (New)   A video data transfer system comprising:

a video processor which receives video data and processes the video data to

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produce processed video data; a display path coupled to the video processor, the display

path including a frame buffer, the display path conveys the processed video data from the

video processor to a display; and

a capturing path coupled to the video processor, the capturing path conveys

the processed data from the video processor to a system memory, the capturing path not

including the frame buffer.

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